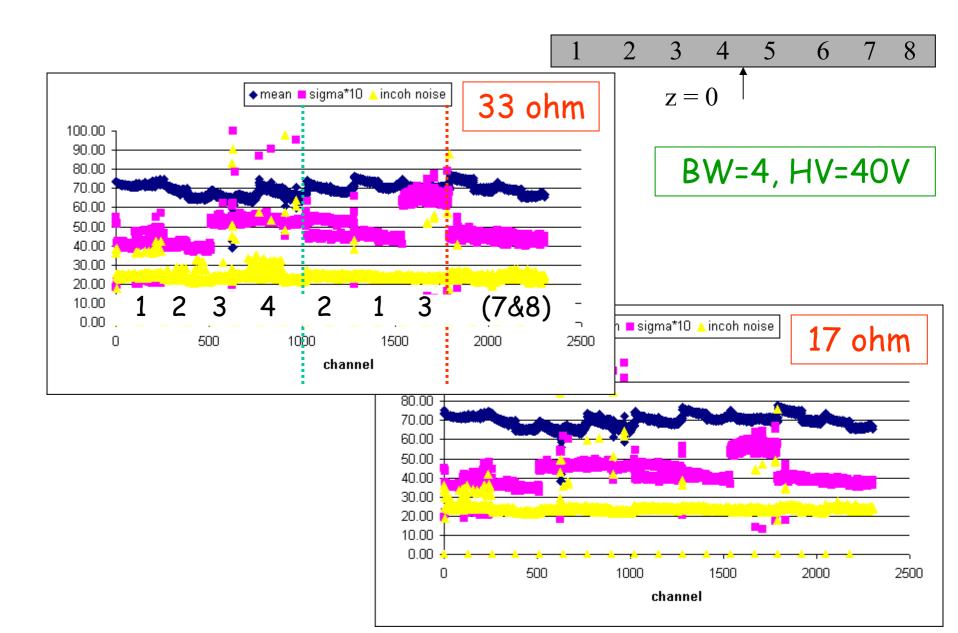
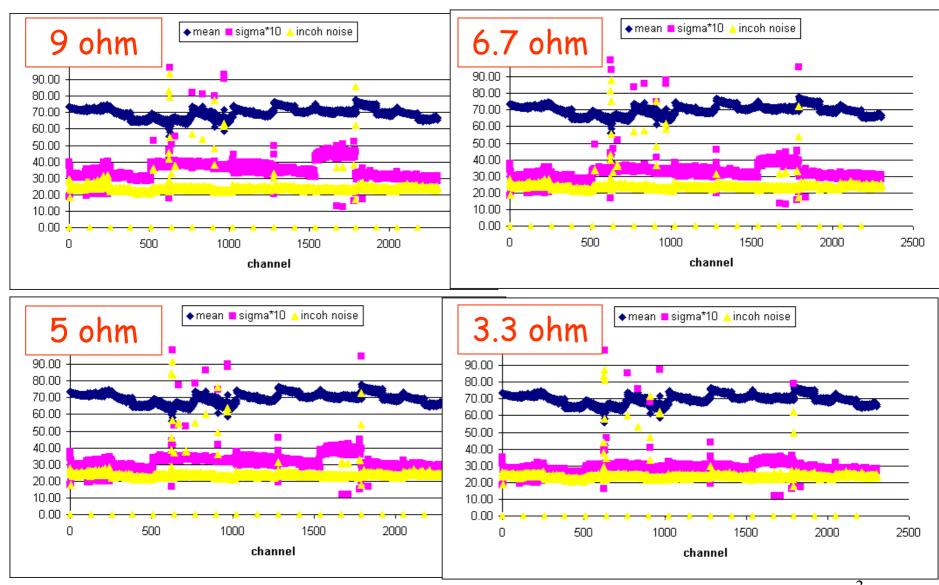


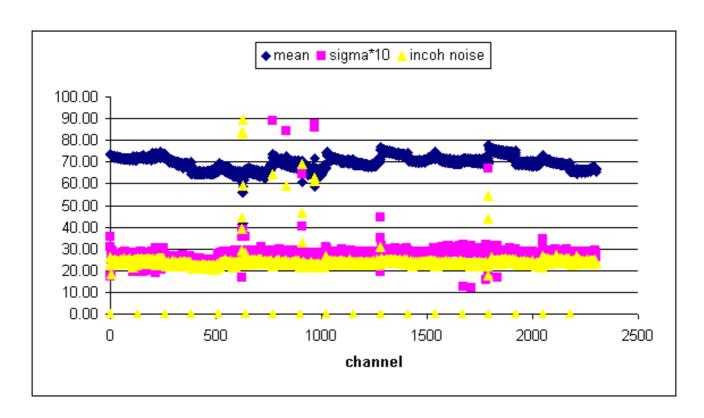
Resistor across isolated and non-isolated GND



Smaller resistor

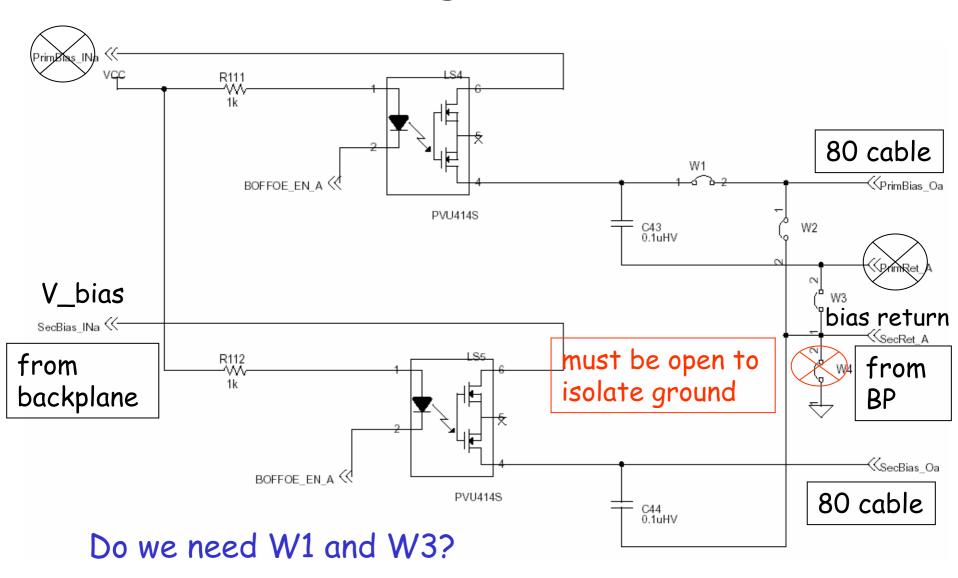


And shorted...



- · No optimum point smaller the better
- · Potential difference seems to create current
 - but we have to isolated them???

Bias configuration in IB



W1&W2&W3 (default) vs W2

